BSPD V2 3P

Three separate boards in accumulator, pedal box and HVD. Main board in HVD monitors outputs from pedalbox and accumulator boards which detail whether the brake of current thresholds have been passed or if there are any open circuit or short circuit implausibility’s.

The two monitor boards have pulldowns on their inputs and buffered outputs. Each board also has led displays to show threshold and OCSC state.

# Pinout Main

* 12 V
* GND
* PRESSURE\_THRESHOLD
* PRESSURE\_~OCSC
* CURRENT\_THRESHOLD
* CURRENT\_~OCSC
* BSPD\_OK

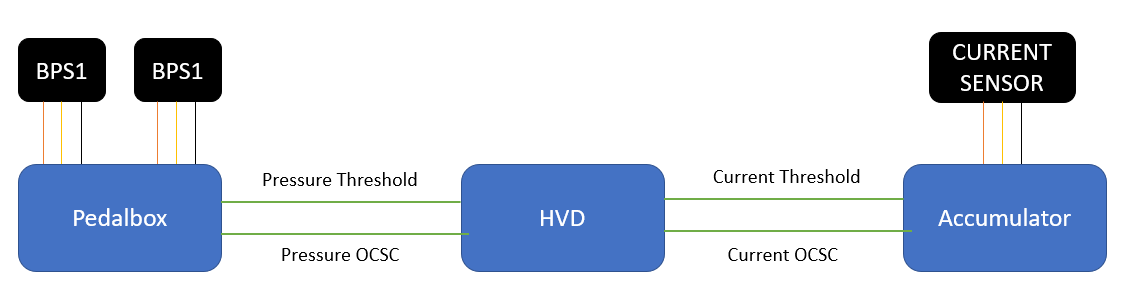
# Pinout Pressure

* 12 V
* GND
* PRESSURE\_THRESHOLD
* PRESSURE\_~OCSC
* BPS1\_SIG
* BPS1\_PWR
* BPS1\_GND
* BPS2\_SIG
* BPS2\_PWR
* BPS2\_GND

# Pinout Current

* 12/5 V
* GND
* CURRENT\_THRESHOLD
* CURRENT\_~OCSC
* CURRENT\_SENSE

# Block Diagram



# Compliance

The set of BSPD units function together to fulfil the BSPD rules **EV 8.7**.